## **HVWS Workshop Schedule at Bangalore Tech Summit**

Zoom conferencing for presenters / remote participants.

Connect to this conference, ( be there early, not too late please).

Join from PC, Mac, Linux, iOS or Android: https://zoom.us/j/262006222?pwd=M2dqaWxwdTE4NGRuNnNyb1NMZHd0dz09

Password is: hvws

Thursday, October 11

TIMESLOT: 2:00 PM to approx. 4:30 PM (flexible up to 5:00 PM) Hypervisor Project Working Session (2.5 hours, incl. breaks)

(= 10:30 AM - 13:00 CEST)

## **TZ Conversion Start Time & End Time**

Bangalore, India	Thu, 11 Oct 2018	14:00	Bangalore, India	Thu, 11 Oct 2018	16:30
Berlin, Germany'	Thu, 11 Oct 2018	10:30	Berlin, Germany'	Thu, 11 Oct 2018	13:00

Topic Block	Timing		
GPU Sharing Block	14:00 local time = 10:30 AM Europe CEST		
OpenSynergy. Based on GPU Sharing results in HV project (remote)	Length: 60 min, hard stop		
Special GPU support discussion (supported by Stephen)			
Memory buffer sharing (ADIT preparing) n-site			
Security Block	Starting 14.45 if needed/possible, see below.		
<ul> <li>TrustZone, Optee</li> <li>Comprehensive presentation by ARM, base on White Paper         <ul> <li>Use-cases for EL2 in the TrustZone</li> <li>ARM architects presenting intro based on White Paper sent to mailing list</li> </ul> </li> <li>HSMs?         <ul> <li>Artem: We are working on something in Xen</li> <li>(not discussed in this workshop)</li> </ul> </li> </ul>	15:00 local time = 11:30 AM Europe CEST  60-75 minutes  ARM presentation expected ~45 minutes?  (☆ remote presentation)  Q&A  Discussion		
★ COFFEE BREAK 15.30-15.45 ★	★SHARED COFFEE BREAK 15.30-15.45 ★ ② Note that ARM presentation will overlap if it's 45 minutes long, so we may need to take a break in the middle.		
Process/VM Scheduling  (not covered in detail)  Quick recap scheduling challenge Big/Little (Munich), but it might be covered in project overview instead  Anup - introduce "CPU stealing" - API between HV&VM for accounting and adjusting to this.  Possibly Xen development / research (Artem)	~ 30 min		
Backup topics     Audio – Feedback on HVWS: Xen input and experience on Audio, Display, Input and TEE?			

## Collected questions for the ARM / Security session.

- ARM architects presenting intro based on White Paper sent to mailing list
   Artem's questions previously sent via email please document them!